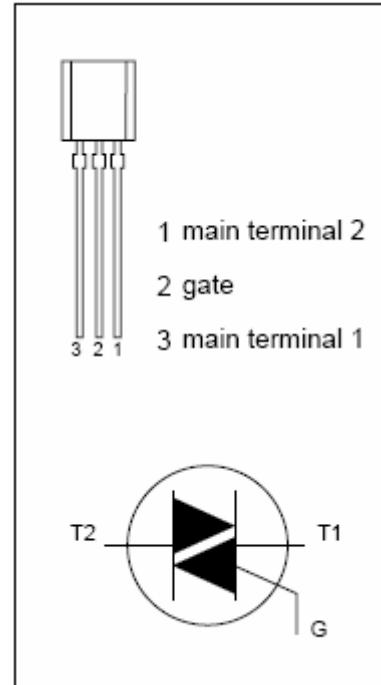


◆ Features

- With TO-92 package
- For use in general purpose bidirectional switching and phase control applications, which is intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

◆ QUICK REFERENCE DATA

SYMBOL	PARAMETER	VALUE	UNIT	
V_{DRM}	Repetitive peak off-state voltage	600	V	
V_{RRM}	Repetitive peak off-state voltage	600	V	
$I_{T(AV)}$	Average on-state current	1	A	
I_{TSM}	Non-repetitive peak on-state current	16	A	
$P_{G(AV)}$	Average gate power	0.5	W	
T_{stg}	Storage temperature	-40~150	°C	
T_j	Operating junction temperature	125	°C	TO-92



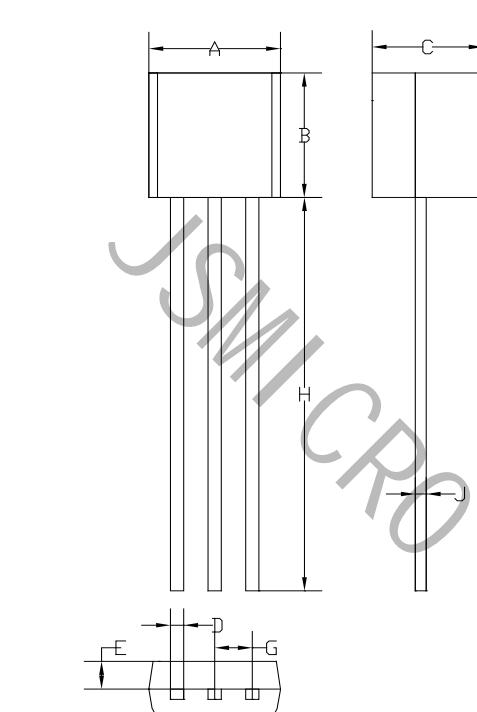
◆ ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$, unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage	$I_D=0.1\text{mA}$	600		V
V_{RRM}	Repetitive peak reverse voltage	$I_D=0.5\text{mA}$	600		V
I_{GT}	Gate trigger current	$V_D=12\text{V}; I_T=0.1\text{A}$ T2+ G+		3	mA
		T2+ G-		3	
		T2- G-		3	
		T2- G+		7	
V_T	On-state voltage	$I_T=2\text{A}$		1.5	V
I_H	Holding current	$V_D=12\text{V}; I_{GT}=0.1\text{A}$		5	mA
V_{GT}	Gate trigger voltage	$V_D=12\text{V}; I_T=0.1\text{A}$ T2+ G+		1.5	V
		T2+ G-		1.5	
		T2- G-		1.5	
		T2- G+		1.5	

PACKAGE OUTLINE

Plastic surface mounted package

TO-92



TO-92		
Dim	Min	Max
A	4.4	4.7
B	4.3	4.7
C	3.43	Typical
D	0.38	0.55
E	1.1	1.4
G	1.27	Typical
H	14.1	14.5
J	0.36	0.51

All Dimensions in mm