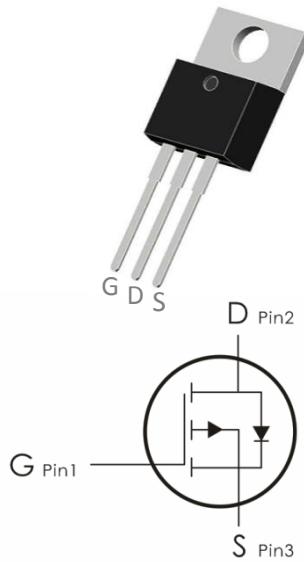


Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=-60V, I_D=-17A, R_{DS(on)}<86m\Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	-17	A
	Continuous Drain Current- $T_C=100^\circ C$	---	
	Pulsed Drain Current ¹	-76	
E_{AS}	Single Pulse Avalanche Energy	---	mJ
P_D	Power Dissipation($T_C=25^\circ C$)	50	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	3	$^\circ C/W$
R_{eJA}	Thermal Resistance,Junction to Ambient	62.5	

Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250 \mu\text{A}$	-60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-60\text{V}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics²						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=-250 \mu\text{A}$	-1	---	-3	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-8\text{A}$	---	61	86	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-6\text{A}$	---	75	125	
G_{FS}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-15\text{A}$	---	10	---	S
Dynamic Characteristics³						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1135	---	pF
C_{oss}	Output Capacitance		---	95	---	
C_{rss}	Reverse Transfer Capacitance		---	60	---	
Switching Characteristics³						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-30\text{V}, I_{\text{D}}=-1\text{A}, R_{\text{GEN}}=6 \Omega, V_{\text{GS}}=-10\text{V}$	---	13	26	ns
t_r	Rise Time		---	4	8	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		---	45	90	ns
t_f	Fall Time		---	6	12	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-3.5\text{A}$	---	22.6	29.4	nC
Q_{gs}	Gate-Source Charge		---	2.4	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	5.7	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-19\text{A}$	---	---	-1.5	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
3. Guaranteed by design, not subject to production.

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

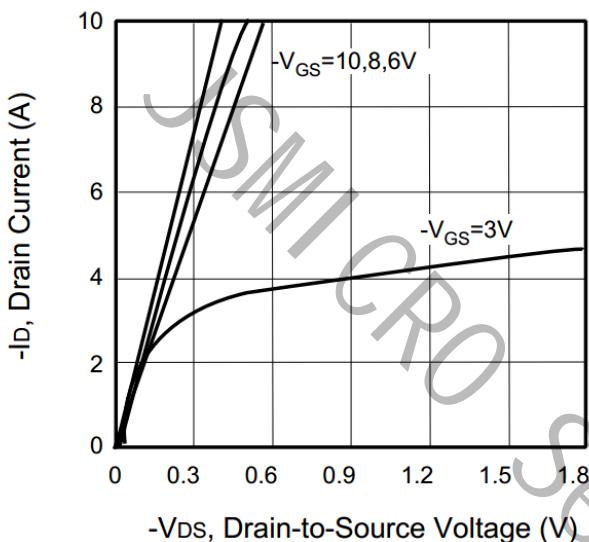


Figure 1. Output Characteristics

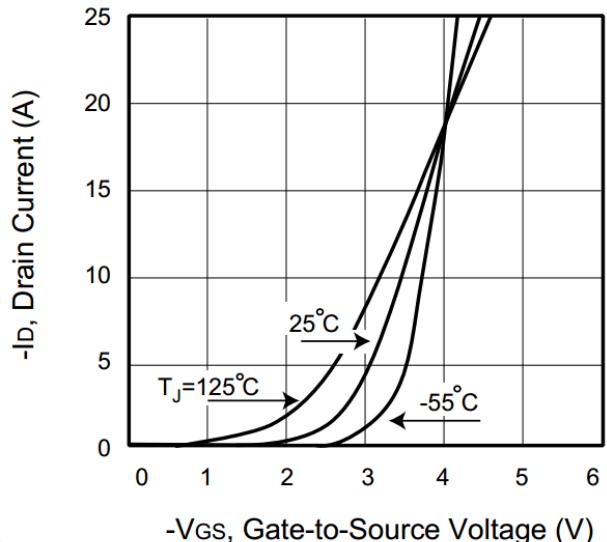


Figure 2. Transfer Characteristics

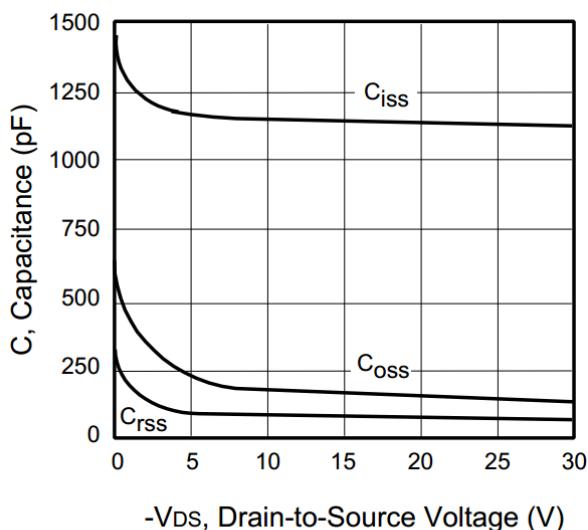


Figure 3. Capacitance

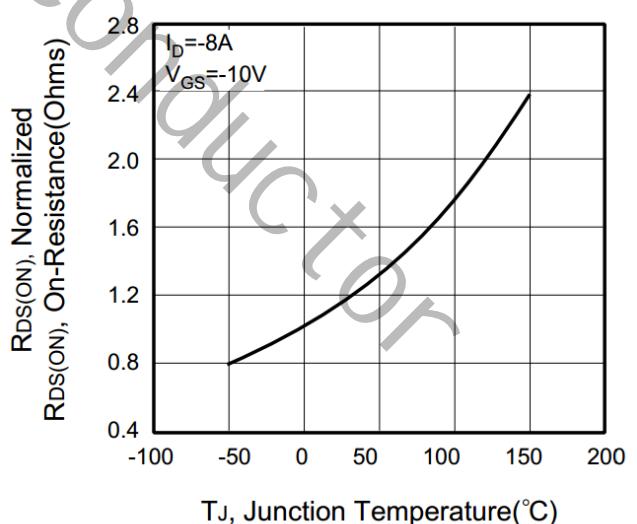


Figure 4. On-Resistance Variation with Temperature

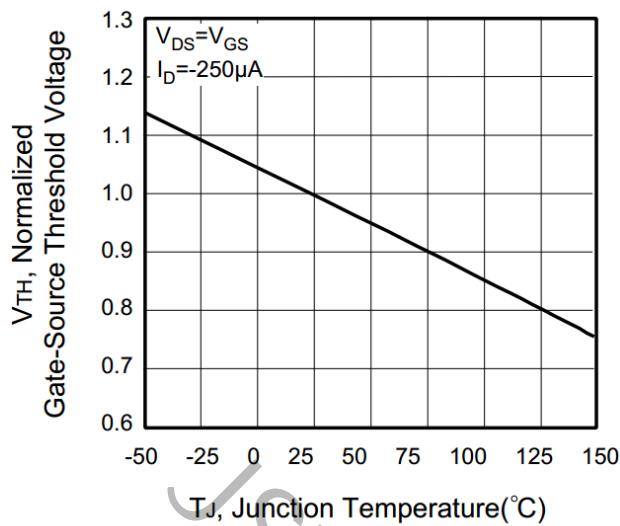


Figure 5. Gate Threshold Variation with Temperature

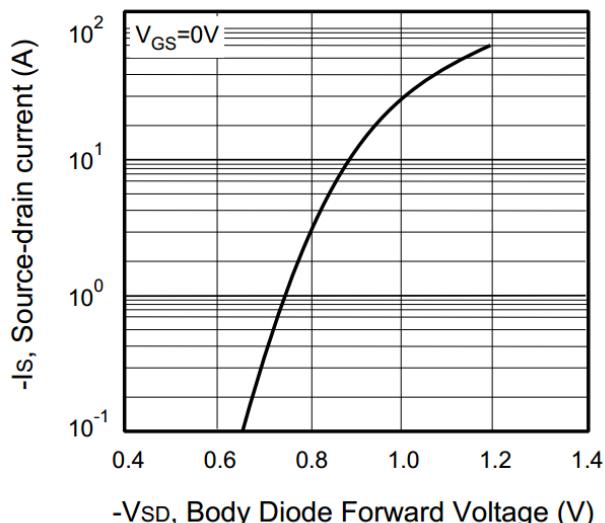


Figure 6. Body Diode Forward Voltage Variation with Source Current

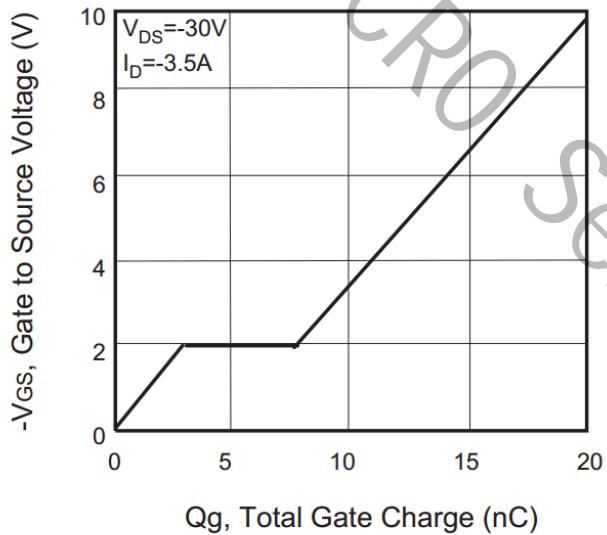


Figure 7. Gate Charge

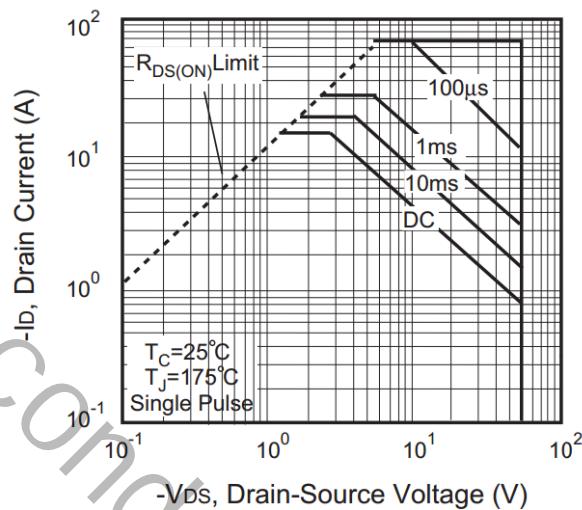


Figure 8. Maximum Safe Operating Area

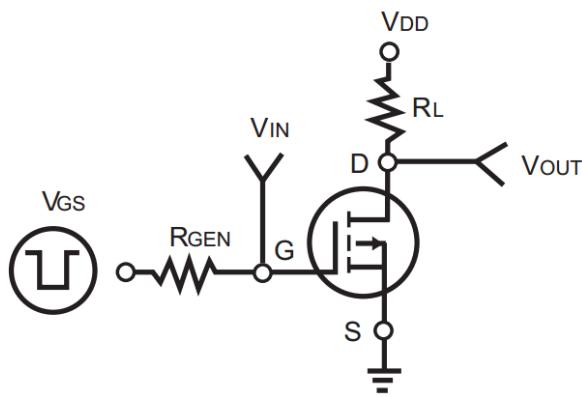


Figure 9. Switching Test Circuit

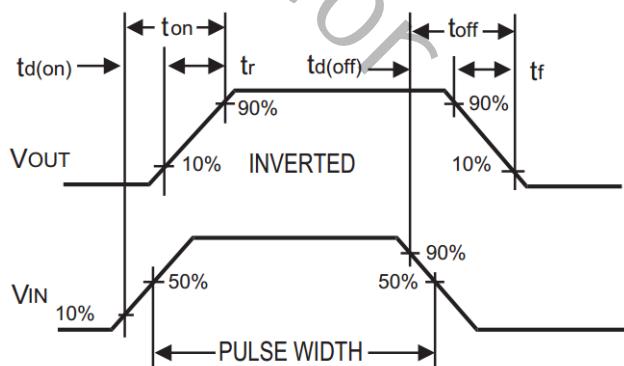


Figure 10. Switching Waveforms

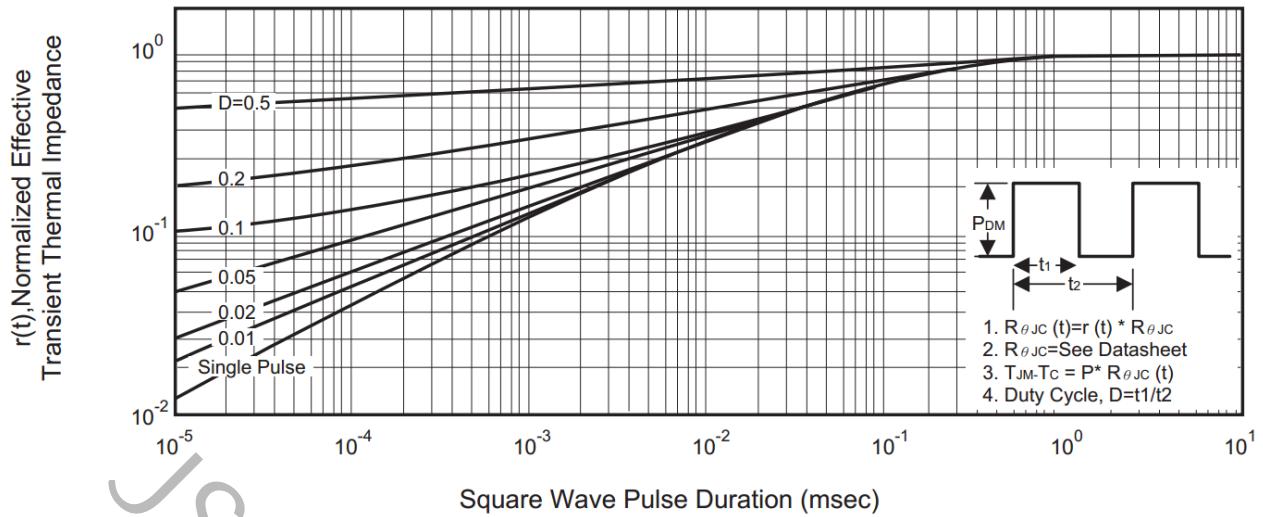


Figure 11. Normalized Thermal Transient Impedance Curve